



Bharati Vidyapeeth's
Institute of Technology (Poly.) Palus

DEPARTMENT OF ELECTRICAL ENGINEERING

THEORY EXAM TIME TABLE SUMMER-2020 (Non-final Backlog subjects)

Date 24-11-2020 (Tuesday)

Time	Subject Code	Subject Name	Scheme
11.00 AM to 12.00 PM	17321	BASIC ELECTRONICS (ELECTRICAL)	EE-3-G
02.00 PM to 03.00 PM	----	----	----

Date 25-11-2020 (Wednesday)

Time	Subject Code	Subject Name	Scheme
11.00 AM to 12.00 PM	17322	ELECTRICAL & ELECTRONIC MEASUREMENT	EE-3-G
02.00 PM to 03.00 PM	17416	INDUSTRY ELECTRICAL SYSTEMS-I	EE-4-G

Date 26-11-2020 (Thursday)

Time	Subject Code	Subject Name	Scheme
11.00 AM to 12.00 PM	17323	ELECTRICAL CIRCUITS AND NETWORKS	EE-3-G
02.00 PM to 03.00 PM	17413	ELEMENTS OF MECHANICAL ENGINEERING	EE-4-G

Date 27-11-2020 (Friday)

Time	Subject Code	Subject Name	Scheme
11.00 AM to 12.00 PM	17324	ELECTRICAL POWER GENERATION	EE-3-G
02.00 PM to 03.00 PM	----	----	----

Date 01-12-2020 (Tuesday)

Time	Subject Code	Subject Name	Scheme
11.00 AM to 12.00 PM	17415	DC MACHINES & TRANSFORMERS	EE-4-G
02.00 PM to 03.00 PM	----	----	----

Date 02-12-2020 (Wednesday)

Time	Subject Code	Subject Name	Scheme
11.00 AM to 12.00 PM	----	----	----
02.00 PM to 03.00 PM	17301	APPLIED MATHEMATICS	EE-3-G

Date 03-12-2020 (Thursday)

Time	Subject Code	Subject Name	Scheme
11.00 AM to 12.00 PM	17414	INDUSTRIAL INSTRUMENTATION	EE-4-G
02.00 PM to 03.00 PM	----	----	----

Date 04-12-2020 (Friday)

Time	Subject Code	Subject Name	Scheme
11.00 AM to 12.00 PM	17417	TRANSMISSION & DISTRIBUTION OF ELECTRICAL POWER	EE-4-G
02.00 PM to 03.00 PM	----	----	----

Head of Department

Handwritten signature and date: 12/11/2020
Verified by: [Signature] 12/11/2020

Principal

Handwritten signature
Principal
Bharati Vidyapeeth's
Institute of Technology (Poly.)
Palus, Tal-Palus, Dist-Sangli.